

EAST - [10674146.wsp:1]

File View Edit Tools Window Help

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DBs US-PGPUB: USPAT: USOCR: EPO: JPO: DERWENT: IBM_TDB ☒ Plurals

Default operator: OR ☐ Highlight all hit terms initially

Drafts

- IS&R:
- BRS:
- BRS: 23

Pending

Active

- L1: (1520) lempel near1 ziv
- L2: (2167456) compress\$4
- L3: (10292) don't near1 care
- L5: (0) 1 with 4
- L6: (1263) 2 with 1
- L7: (10950) don\$4 near1 care
- L8: (10950) 3 7
- L10: (0) 1 with 9
- L4: (71) 2 near9 3
- L11: (0) 1 with 3
- L12: (629) 1 with algorithm
- L13: (0) 8 with 12
- L15: (1348) lempel near9 2
- L16: (0) 8 with 15
- L14: (556) 2 with 12
- L17: (111) 2 with 7
- L9: (111) 2 with 8
- L18: (0) 4 NOT 9
- L19: (0) lempel WITH 8
- L20: (34) lempel AND 8
- L21: (73) 8 NEAR9 COMPRESS\$4
- L22: (0) 21 NOT 17
- L23: (2698) 8 NEAR9 BIT\$2
- L24: (17) COMPRESS\$5 NEAR9 23
- L25: (10) BUFFER WITH 14

Failed

Saved

Favorites

Tagged (10)

UDC

Queue

Trash

BRS form IS&R form Image Text HTML

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050144387 A1	20050630	20	Mechanism to include hints within compressed data	711/118	711/128		Adl-To et al.
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5740407 A	19980414	10	Method of generating power vectors for circuit power	703/13			Yeap;
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6708305 B1	20040316	27	Deterministic random LBIST	714/739	714/728		Farnsw al.
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050071562 A1	20050331	15	Mechanism to compress data in a cache	711/118			Adl-To et al.
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7043079 B2	20060509	23	"Don't care" pixel interpolation	382/195	348/586; 358/448;		Malva
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050160234 A1	20050721	39	Multi-processor computing system that employs	711/144	711/133; 711/145		Newb
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050071566 A1	20050331	20	Mechanism to increase data compression in a cache	711/128			Adl-To et al.
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5874907 A	19990223	10	Method and apparatus for providing improved data	341/51	341/63		Craft;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 5745504 A	19980428	22	Bit error resilient variable length code	714/752	341/67; 341/94;		B.ang
10	<input type="checkbox"/>	<input type="checkbox"/>	US 3490690 A	19700120	21	DATA REDUCTION SYSTEM	341/67	341/89		LITTLE

Hits Details HTML

Ready

CAP NUM



BRS:

BRS: 23

☐ BRS form ☐ IS&R form ☐ Image ☐ Text ☐ HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20050229062	20051013	16	Systems and methods for proce	714/738			Volker
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030131298	20030710	19	Test pattern compression for an	714/738			Rajski,
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20020162066	20021031	15	Test vector compression metho	714/738			Khoch
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20020053057	20020502	19	Test pattern compression for an	714/738			Rajski,
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6732312 B2	20040504	14	Test vector compression metho	714/738	714/728		Khoch
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6543020 B2	20030401	20	Test pattern compression for an	714/738			Rajski;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6327687 B1	20011204	20	Test pattern compression for an	714/738			Rajski;
12	<input type="checkbox"/>	<input type="checkbox"/>	US 20040148554	20040729	51	Accelerated scan circuitry and	714/727			Dervis
13	<input type="checkbox"/>	<input type="checkbox"/>	US 20060095818	20060504	19	System and method for autom	714/726			Bratt;
14	<input type="checkbox"/>	<input type="checkbox"/>	US 20050154948	20050714	49	Accelerated scan circuitry and	714/726			Dervis
15	<input type="checkbox"/>	<input type="checkbox"/>	US 20050028060	20050203	49	Accelerated scan circuitry and	714/726			Dervis
16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050160234	20050721	39	Multi-processor computing syste	711/144	711/133;		Newbi
17	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050071566	20050331	20	Mechanism to increase data co	711/128			Adl-To
18	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050144387	20050630	20	Mechanism to include hints with	711/118	711/128		Adl-To
19	<input type="checkbox"/>	<input type="checkbox"/>	US 20050144386	20050630	18	Mechanism to store reordered	711/118	711/128;		Adl-To
20	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050071562	20050331	15	Mechanism to compress data in	711/118			Adl-To
21	<input type="checkbox"/>	<input type="checkbox"/>	US 20030070039	20030410	17	Circuit and method for performi	711/108			Gilling
22	<input type="checkbox"/>	<input type="checkbox"/>	US 6708250 B2	20040316	17	Circuit and method for performi	711/108	365/49		Gilling
23	<input type="checkbox"/>	<input type="checkbox"/>	US 20060112119	20060525	23	System, method and computer	707/101			Vian;
24	<input type="checkbox"/>	<input type="checkbox"/>	US 20020013775	20020131	22	METHOD OF PROCESSING A RE	706/48			SKOV
25	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5740407 A	19980414	10	Method of generating power v	703/13			Yeap;
26	<input type="checkbox"/>	<input type="checkbox"/>	US 4999792 A	19910312	13	Method and apparatus for auto	700/276	431/2		Ander
27	<input type="checkbox"/>	<input type="checkbox"/>	US 6027529 A	20000222	13	Protheses with selectively welde	623/1.53	623/23.7		Roych
28	<input type="checkbox"/>	<input type="checkbox"/>	US 5531076 A	19960702	7	Multi-split fan control	62/184	62/428		Pellent
29	<input type="checkbox"/>	<input type="checkbox"/>	US 6904182 B1	20050607	16	Whiteboard imaging system	382/284	382/274;		Simarc
30	<input type="checkbox"/>	<input type="checkbox"/>	US 20060083439	20060420	21	"Don't care" pixel interpolation	382/261	382/173		Malva
31	<input type="checkbox"/>	<input type="checkbox"/>	US 6941024 B2	20050906	29	Coder matched layer separatio	382/248	375/240.19;		Mukhe
32	<input type="checkbox"/>	<input type="checkbox"/>	US 6862371 B2	20050301	16	Method of compressing images	382/248	382/232;		Mukhe
33	<input type="checkbox"/>	<input type="checkbox"/>	US 20030202709	20031030	26	Clustering	382/243	382/218		Simarc
34	<input type="checkbox"/>	<input type="checkbox"/>	US 20030185454	20031002	21	System and method for image	382/240			Simarc
35	<input type="checkbox"/>	<input type="checkbox"/>	US 6917717 B2	20050712	21	System and method for image	382/240	382/248		Simarc
36	<input type="checkbox"/>	<input type="checkbox"/>	US 20040213469	20041028	12	Method for compressing images	382/239	382/166;		Apost
37	<input type="checkbox"/>	<input type="checkbox"/>	US 20030133617	20030717	30	Coder matched layer separatio	382/239	382/264		Mukhe
38	<input type="checkbox"/>	<input type="checkbox"/>	US 20030123740	20030703	17	Method of compressing images	382/239			Mukhe
39	<input type="checkbox"/>	<input type="checkbox"/>	US 6044177 A	20000328	17	Artifact reduction decompressi	382/233	358/525;		Herley
40	<input type="checkbox"/>	<input type="checkbox"/>	US 20050271281	20051208	26	Clustering	382/225	382/218		Simarc
41	<input type="checkbox"/>	<input type="checkbox"/>	US 20030202700	20031030	22	"Don't care" pixel interpolation	382/195			Malva
42	<input type="checkbox"/>	<input type="checkbox"/>	US 20030202699	20031030	25	System and method facilitating	382/195			Simarc
43	<input type="checkbox"/>	<input type="checkbox"/>	US 20030202698	20031030	19	Block retouching	382/195			Simarc
44	<input type="checkbox"/>	<input type="checkbox"/>	US 20030202697	20031030	36	Segmented layered image syst	382/195			Simarc
45	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7043079 B2	20060509	23	"Don't care" pixel interpolation	382/195	348/586;		Malva
46	<input type="checkbox"/>	<input type="checkbox"/>	US 7024039 B2	20060404	19	Block retouching	382/195	348/586;		Simarc
47	<input type="checkbox"/>	<input type="checkbox"/>	US 20030123729	20030703	23	Coder matched layer separatio	382/176			Mukhe
48	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 7027647 B2	20060411	23	Coder matched layer separatio	382/173	382/283		Mukhe
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5838818 A	19981117	12	Artifact reduction compression	382/166	382/232		Herley
50	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5111411 A	19920505	17	Object sorting system	382/141	382/194		Brown
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050180597	20050818	16	Whiteboard imaging system	382/100	345/156		Simarc
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4740993 A	19880426	12	Digital companding circuit	375/249	370/202		Skubni
53	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030202602	20031030	12	Compression of Images and ima	375/240.24	375/240.12;		Apost
54	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050025232	20050203	23	Apparatus, system and method	375/240			Parida
55	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6166942 A	20001226	21	Embedded DRAM architecture	365/63	365/189.02;		Vo; Hu
56	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6141286 A	20001031	21	Embedded DRAM architecture	365/230.03	365/230.06;		Vo; Hu
57	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030214865	20031120	19	Semiconductor memory having	365/200			Blodge
58	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030026140	20030206	19	Semiconductor memory having	365/200			Blodge
59	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020031022	20020314	19	Semiconductor memory having	365/200	365/230.03;		Blodge
60	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6826098 B2	20041130	17	Semiconductor memory having	365/200	365/230.03		Blodge
61	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6587386 B2	20030701	16	Semiconductor memory having	365/200	365/210		Blodge
62	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6434067 B1	20020813	16	Semiconductor memory having	365/200	365/210		Blodge
63	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6307795 B1	20011023	18	Semiconductor memory having	365/200	365/210;		Blodge
64	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6163489 A	20001219	18	Semiconductor memory having	365/200	365/210		Blodge
65	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050068308	20050331	17	Whiteboard imaging system	365/175			Simarc
66	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 3490690 A	19700120	21	DATA REDUCTION SYSTEM	341/67	341/89		LITTLE
67	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5977890 A	19991102	27	Method and apparatus for data	341/55	382/181;		Rigout
68	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6205714 B1	20011002	14	Process for fabricating a mem	341/51	341/17		Reich

☐ Hits ☐ Details ☐ HTML

• L29: (0) 26 WITH 2

Tagged (10)

26 WITH 2

 BRS form
 IS&R form
 Image
 Text
 HTML

10759755_CLS1.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10759755 on February 14, 2006

Original Classifications

17 326/38
8 326/41
6 326/39
6 716/16
5 365/201
3 326/40
3 341/67
3 703/23
3 710/104
3 713/1
3 716/17
2 326/37
2 326/46
2 341/59
2 365/189.08
2 365/200
2 365/233
2 365/49
2 711/171
2 714/32
2 714/725

FPGA
PLD

Cross-Reference Classifications

21 326/39
15 326/41
12 326/38
12 326/40
7 365/189.08
6 365/189.02
4 365/189.01
4 365/189.07
4 365/230.05
4 711/103
3 323/354
3 326/37
3 326/44
3 326/8
3 703/23
3 711/170
3 716/18
2 326/46
2 326/50
2 326/97
2 327/203
2 341/106
2 341/55
2 365/185.01
2 365/185.08
2 365/185.24
2 365/185.33
2 365/189.03
2 365/189.09
2 365/200
2 365/228
2 365/230.02
2 365/230.06
2 710/8
2 711/105

710/8-11
62-74
716/17-18
710/68, 8,
716/17
382/244
341/95
55, 67
87-95
382/232-253
714/738, 927

2 711/106
2 711/115
2 712/248
2 713/100
2 714/31
2 714/38
2 714/701
2 714/725
2 716/16

Combined Classifications

29 326/38
27 326/39
23 326/41
15 326/40
9 365/189.08
8 716/16
7 365/189.02
6 365/201
6 703/23
5 326/37
5 365/189.01
5 365/230.05
5 711/103
4 326/44
4 326/46
4 326/8
4 365/189.07
4 365/200
4 713/1
4 714/725
4 716/17
3 323/354
3 341/67
3 365/185.01
3 365/185.33
3 365/233
3 365/49
3 710/104
3 711/170
3 713/100
3 716/18
2 326/50
2 326/97
2 327/203
2 340/825.52
2 341/106
2 341/51
2 341/55
2 341/59
2 341/63
2 348/699
2 365/185.08
2 365/185.22
2 365/185.24
2 365/189.03
2 365/189.04
2 365/189.09
2 365/228
2 365/230.01
2 365/230.02
2 365/230.06
2 375/240.16

10759755_CLS1.txt

2 710/1
2 710/8
2 711/105
2 711/106
2 711/115
2 711/171
2 711/172
2 711/202
2 712/248
2 714/31
2 714/32
2 714/38
2 714/701
2 714/719
2 714/724